

Docket No: HAMANN  
Appl. No: 09/973,487

**AMENDMENTS TO THE SPECIFICATION WITH MARKINGS TO SHOW  
CHANGES MADE**

Amend the following paragraph(s):

**[0006]** -- FIG. 2 shows the asymmetric equivalent circuit diagram of the electric circuit illustrated in FIG. 1. The inductance of each coil 1 is designated with reference character "L", whereby the individual inductances L are connected in series, as is evident from FIG. 1. Each coil 1 is capacitively coupled to the pole core 5 (see FIG. 3) (~~see FIG. 2~~) on which the coil 1 is mounted. The respective capacitances C are illustrated in FIG. 2 as bypass capacitors C connected to ground, whereby the ground is formed by the magnetic core. Voltage U is applied to the input terminal 2.--.